

FIGURE 1 - PRIOR ART

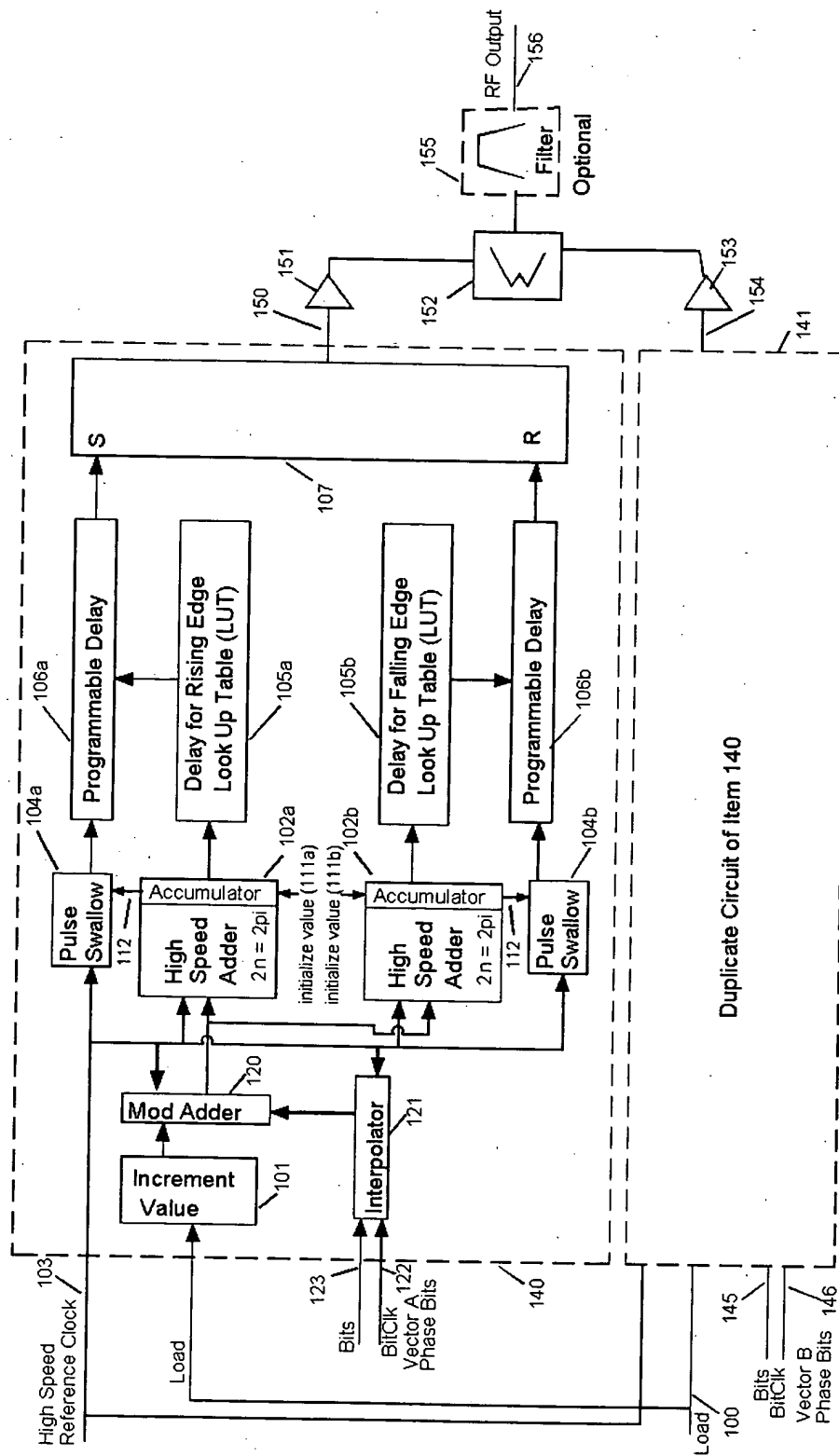
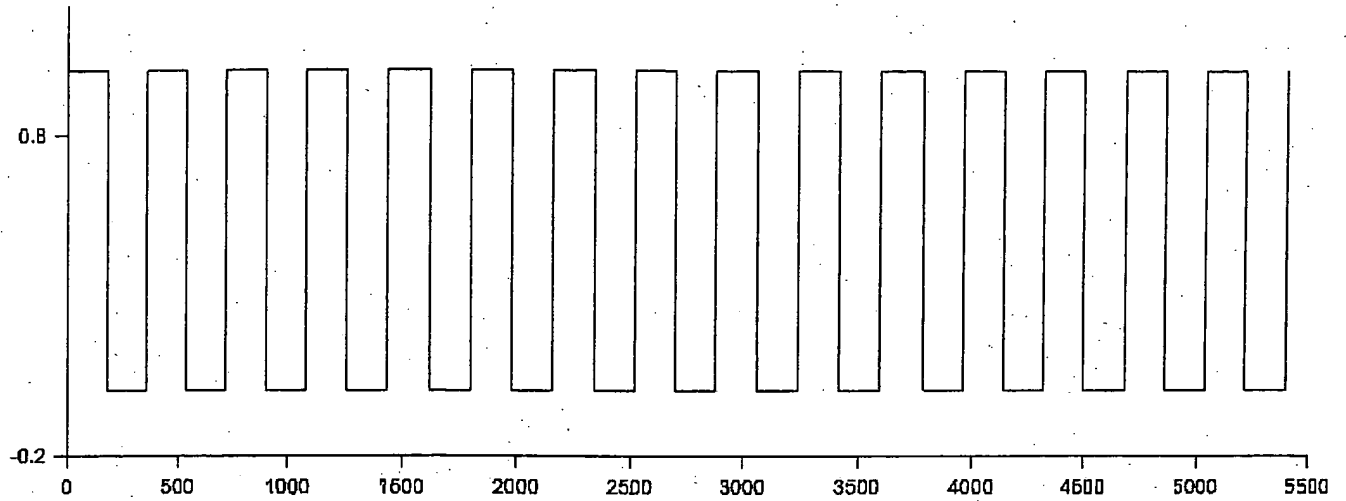


FIGURE 2

Reference Clock



RF Output

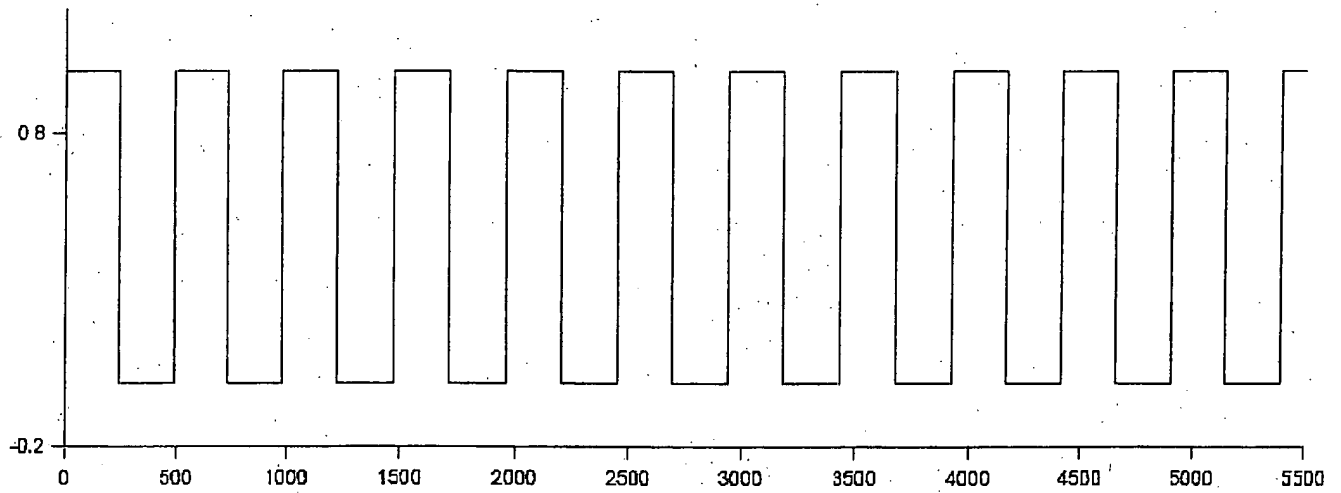


FIGURE 3

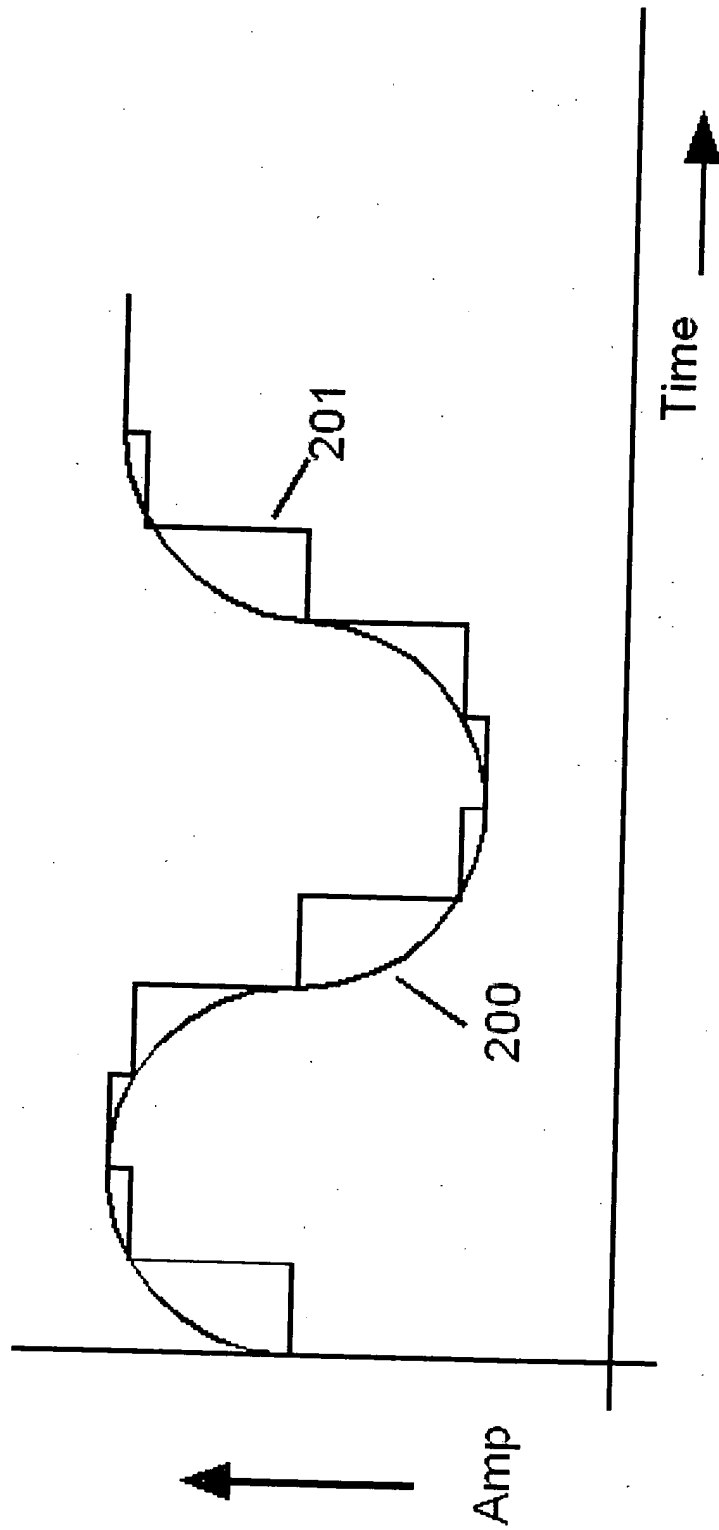


FIGURE 4

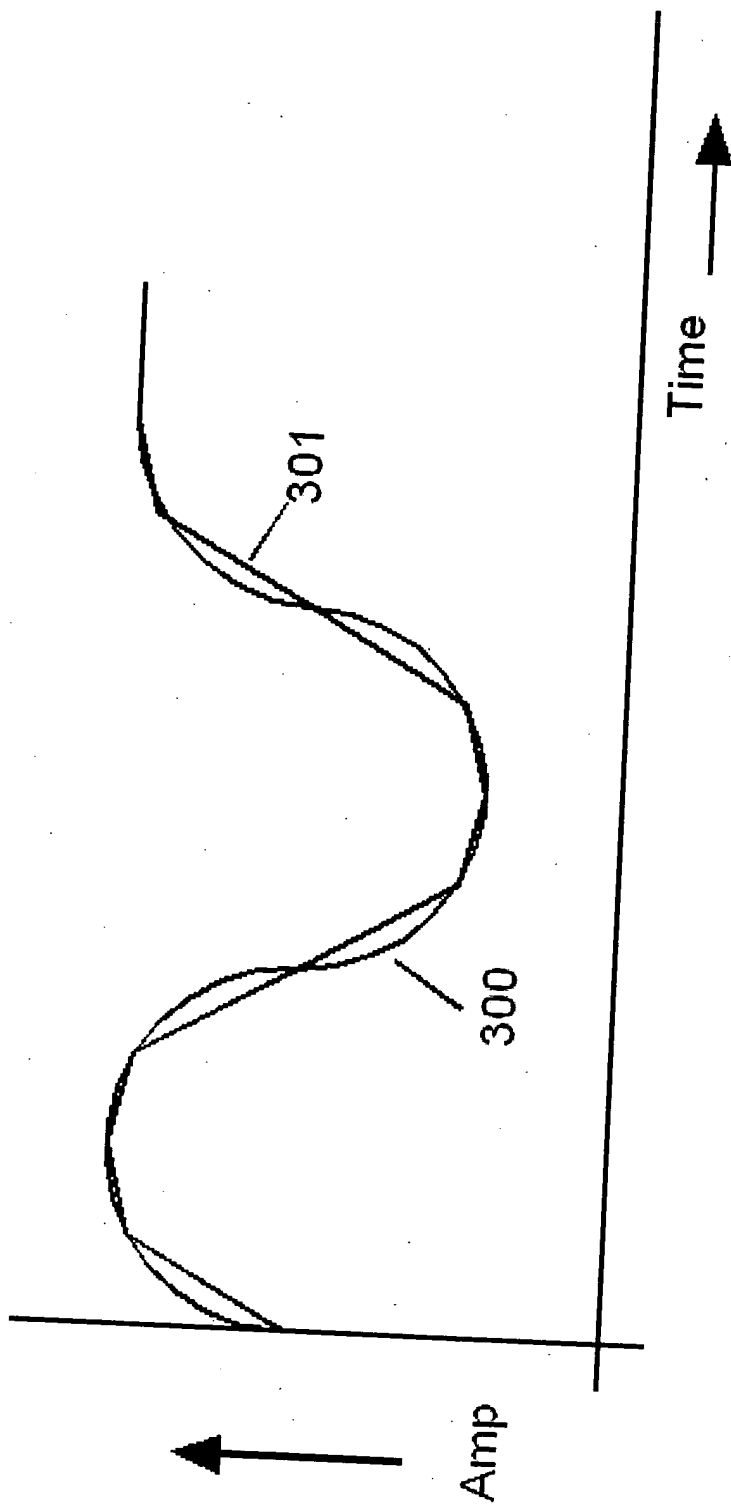


FIGURE 5

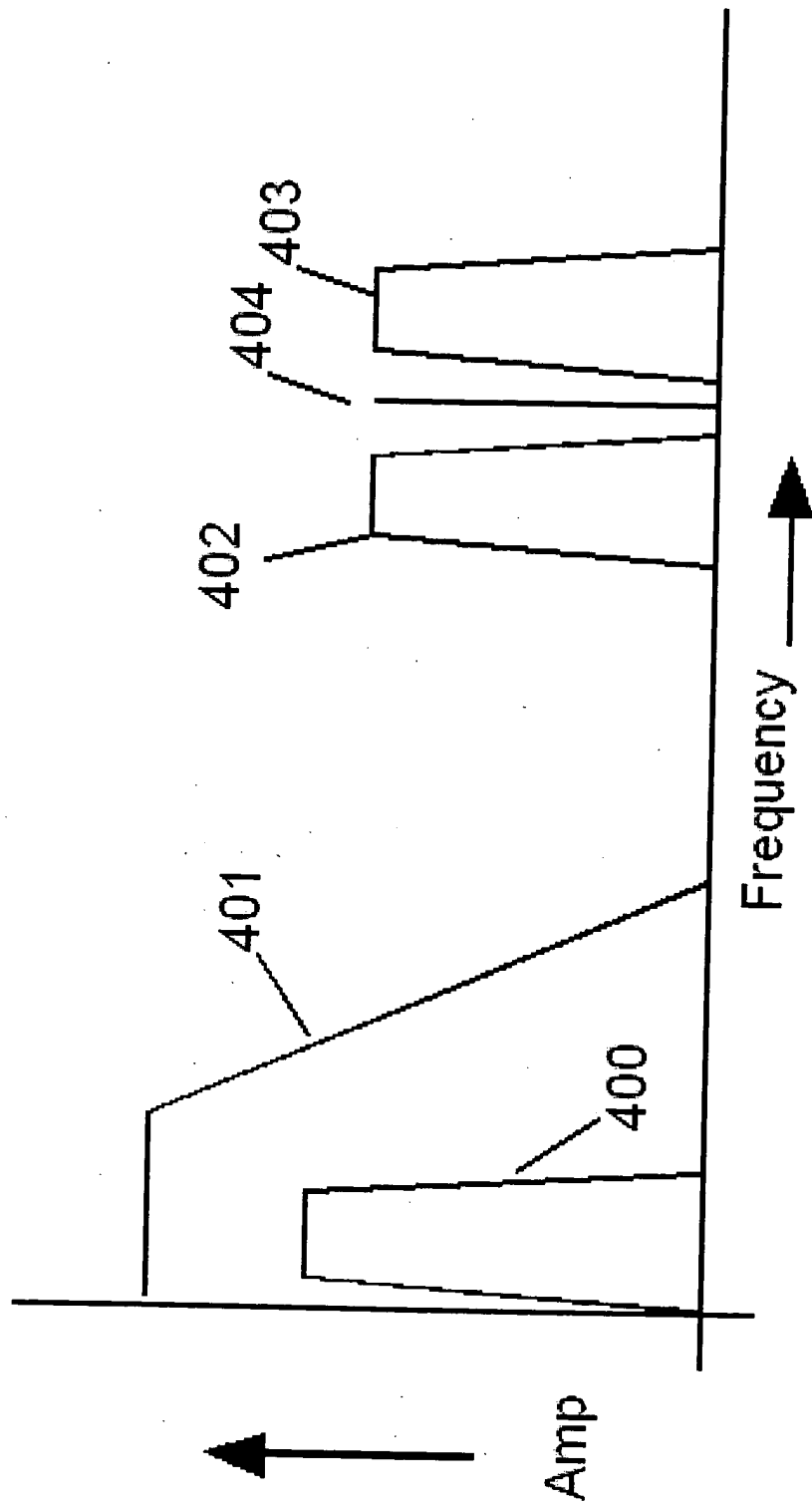


FIGURE 6